0 3 2002





501.39149X00

January 3, 2002

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

NAKAMURA et al.

Serial No.:

09/708,450

Filed:

November 9, 2000

For:

November 9, 2000

A SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

AND THE METHOD THEREOF

Group:

2811

Examiner:

C. Nguyen

## RESPONSE TO RESTRICTION REQUIREMENT

Assistant Commissioner

for Patents

Washington, D.C. 20231

Sir:

In response to the restriction requirement dated December 3, 2001, applicants hereby elect the Group II, claims 20-33, without traverse.

If the Examiner believes that there are any other points which may be clarified or otherwise disposed of, either by telephone discussion or by personal interview, the Examiner is invited to contact applicants' undersigned attorney at the number indicated below.

It is respectfully requested that any shortage in the fee be charged to the account of Antonelli, Terry, Stout & Kraus, LLP, Account No. 01-2135 (Case No. 501.39149X00).

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP

GEM/kd

703/312-6600

Gregory E. Montone

Registration No. 28,141